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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PRACE Pplication of Peter K. Craft, et al.

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09/970,124

Filing Date:

October 2, 2001

Examiner:

Unknown

Atty. Docket No.:

ALA-020

GAU:

2154

For:

NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES

SOLICITED SESSION LAYER READ COMMANDS

March 18, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

8th Supplemental Information Disclosure Statement per 37 C.F.R. §1.98

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring sixteen documents listed on an enclosed one-page form PTO-1449 to the attention of the Examiner in the above-identified application.

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Copies of the documents listed on the enclosed one-page form PTO-1449 are not submitted because they were submitted in an earlier application (09/675,700) which is relied upon for an earlier filing date under 35 U.S.C. §120.

Respectfully submitted,

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on March 18, 2005.

Date: 3-18-05

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U.S. Department of Commerce, Patent and Trademark Office						Application No.: 09/970,124		
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS					Filing da	Filing date: October 2, 2001 Inventors: Peter K. Craft, et al.		
					Inventor			
					Group Art Unit: 2154			
					Examiner name: Unknown			
					Attorney Docket No. ALA-020			
			U.S.	. Patent Documents				
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriat	
11110101	A	5,524,250	6/4/96	Chesson et al.	395	775	ir nppropriat	
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*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)						
	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.						
	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.						
	3	Chesson et al., IEEE Syposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.						
	4	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.						
	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.						
	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.						
	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.						
	8	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.						
	9	Lilienkamp et a	l., Publication e	ntitled "Proposed Host-F	ront End P	rotocol", 56 p	ages, Dec. 1984.	
xaminer			Date Considered					
EXAMINER:	Initial if re	ference considered	I, whether or not o	citation is in conformance or with your communication	with MPEP 6	609; Draw line t	hrough citation if no	